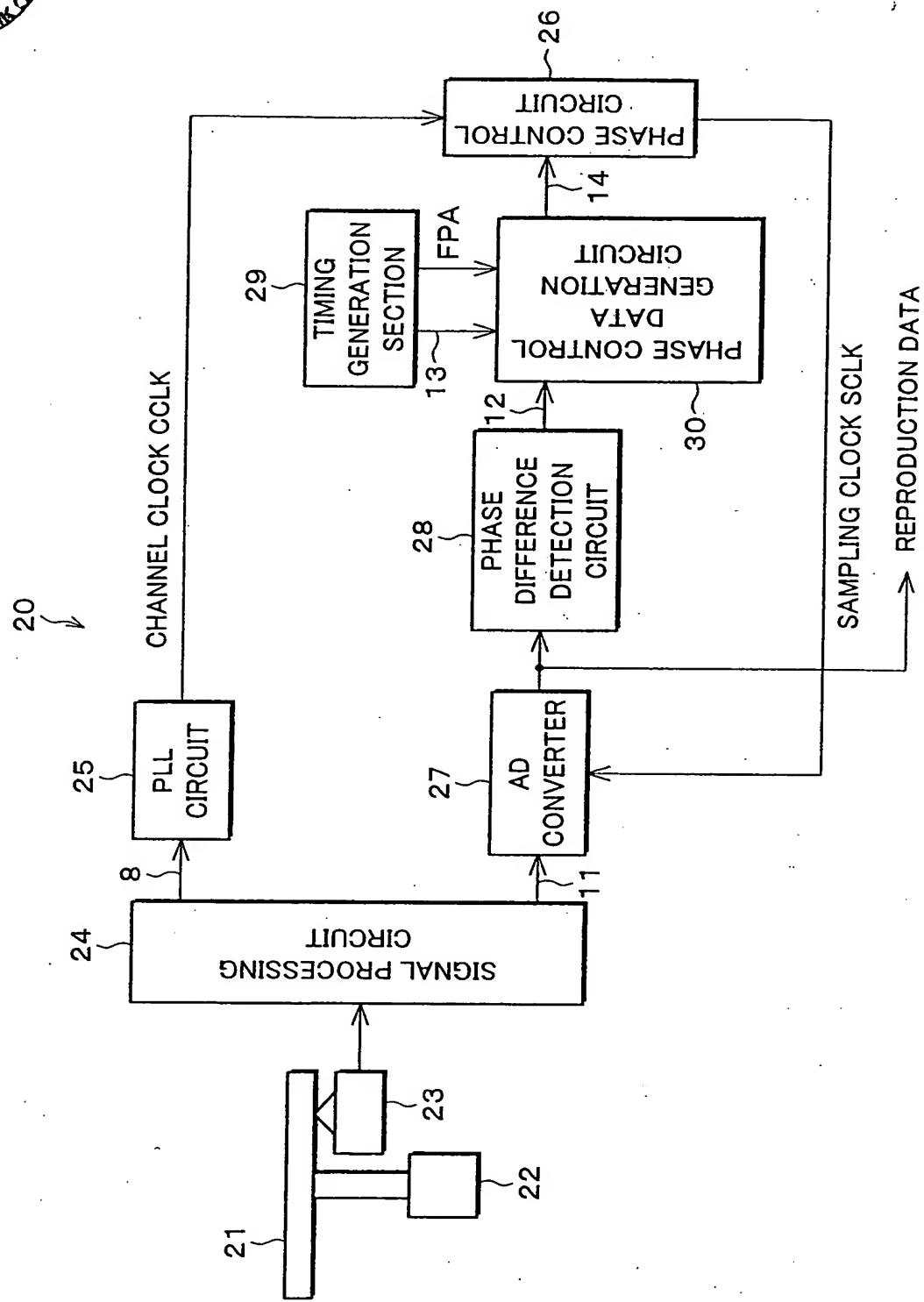




FIG. 1





**FIG. 2**

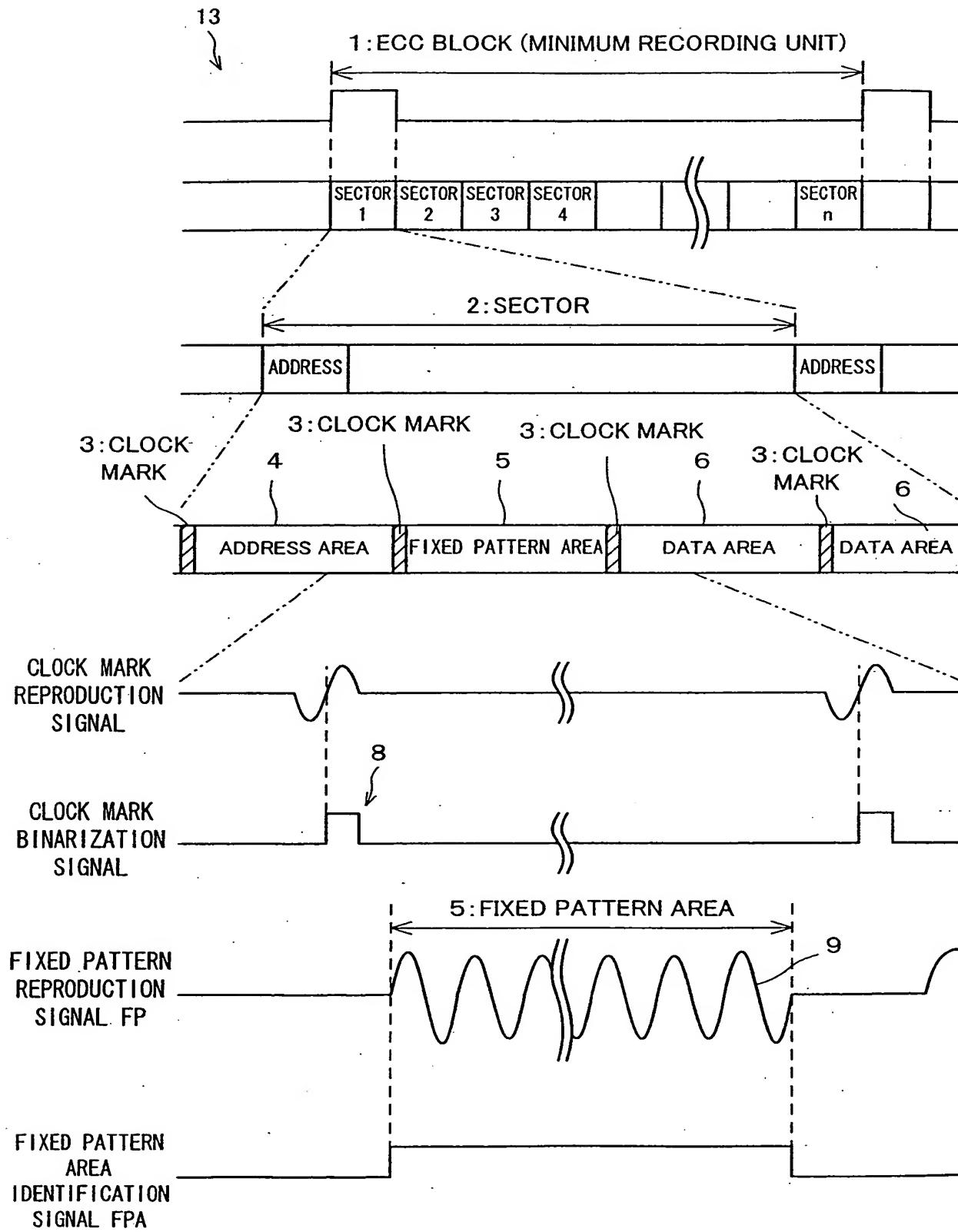
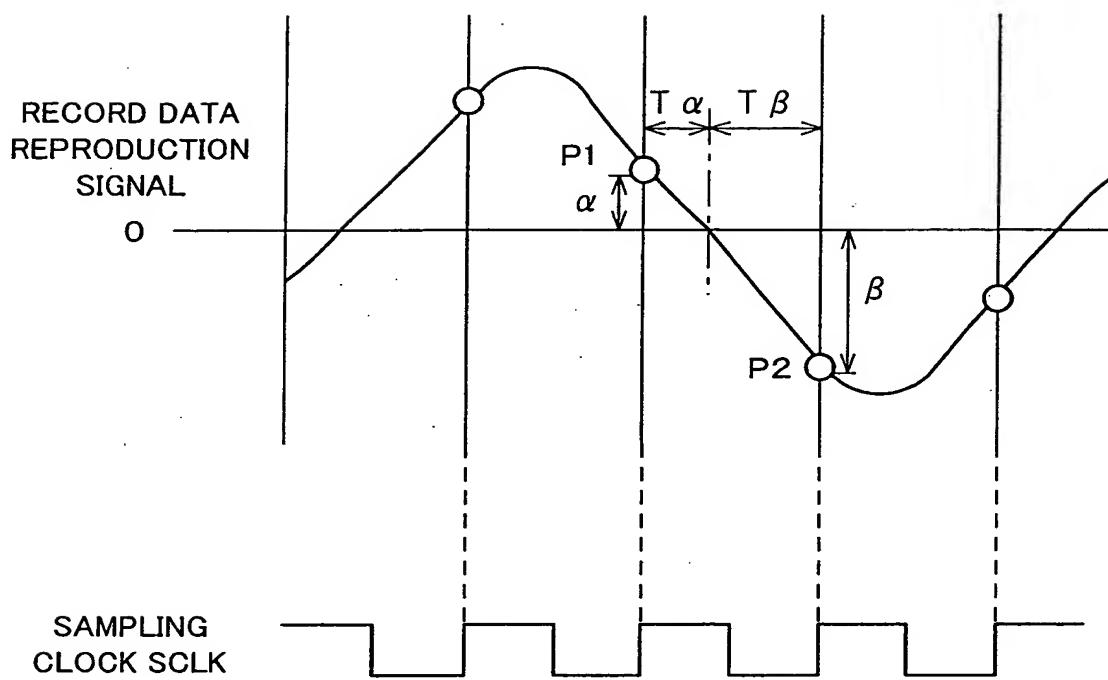




FIG. 3





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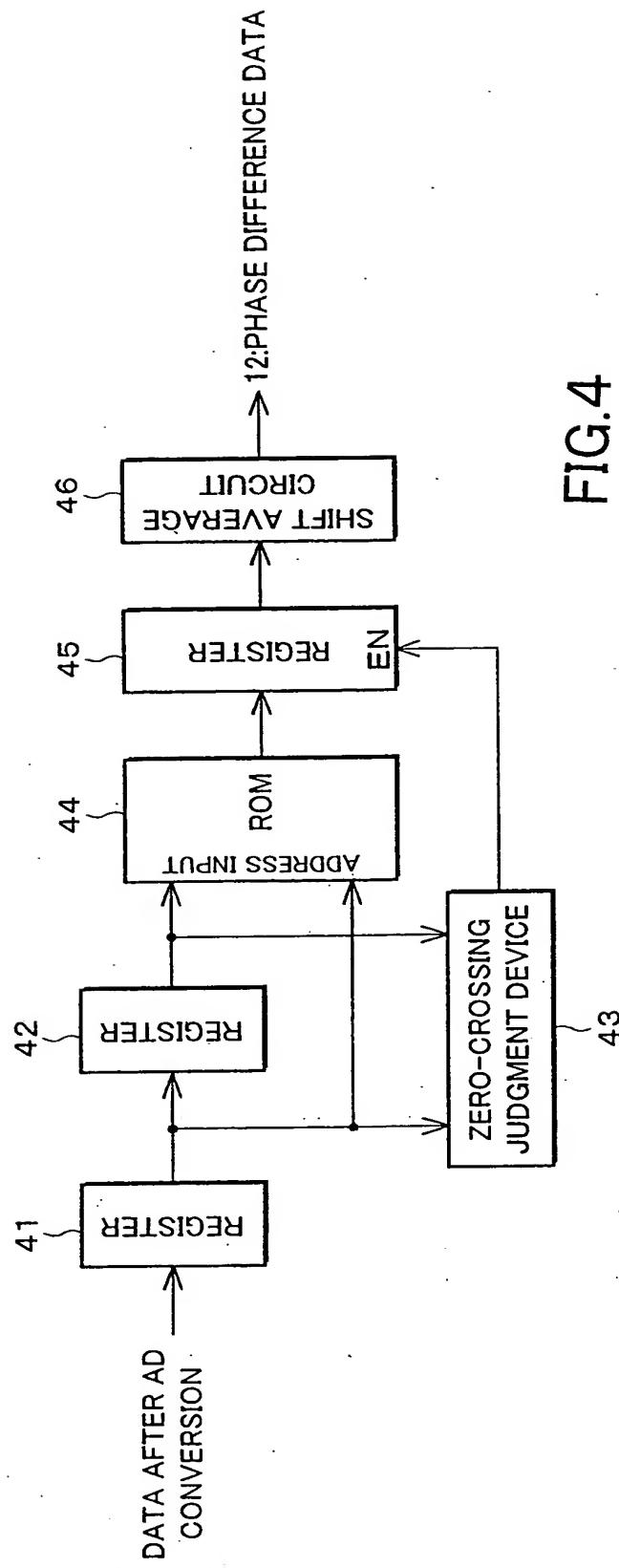
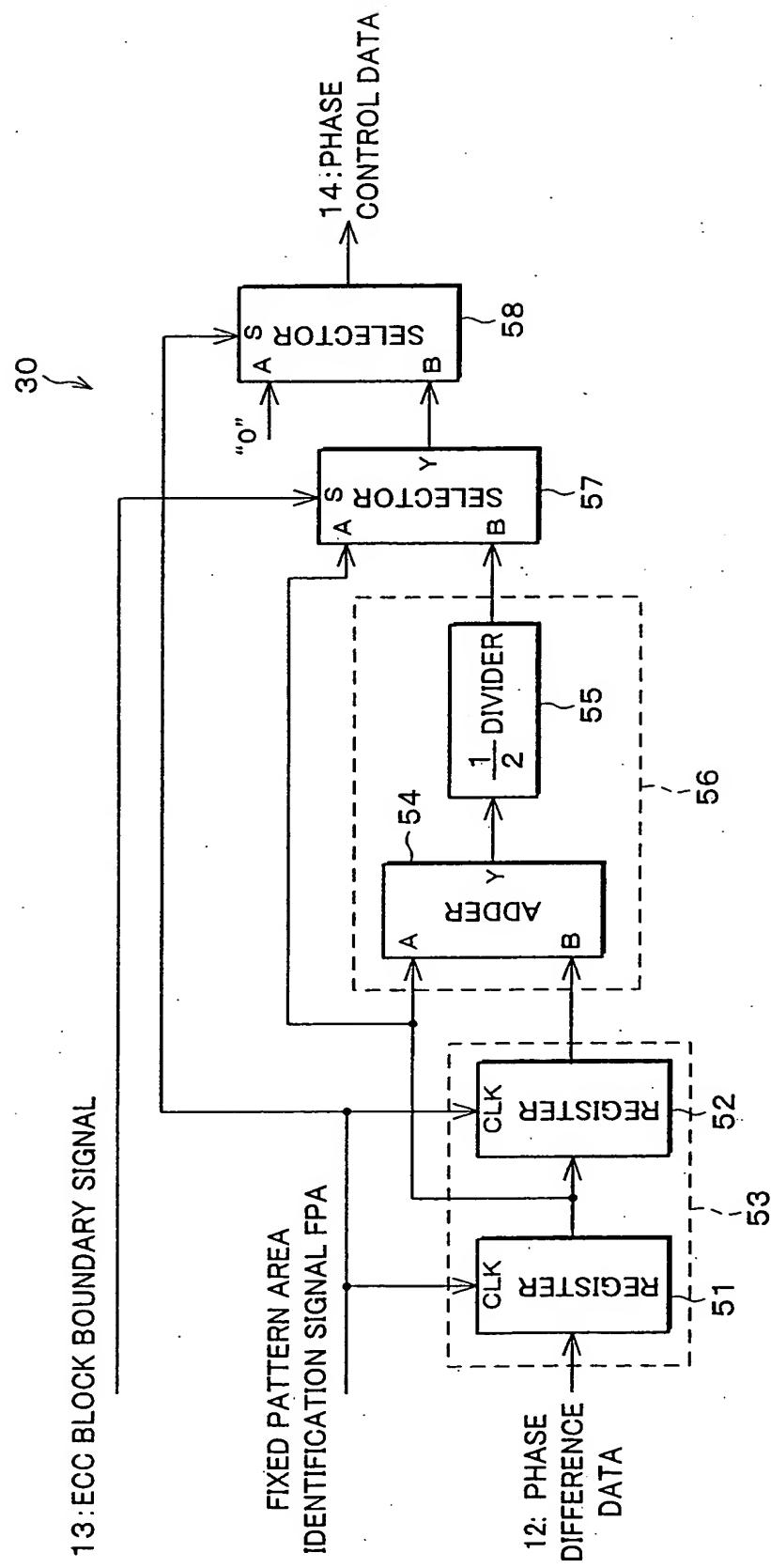


FIG. 4



FIG.5

13: ECC BLOCK BOUNDARY SIGNAL



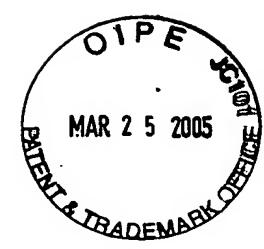


FIG. 6

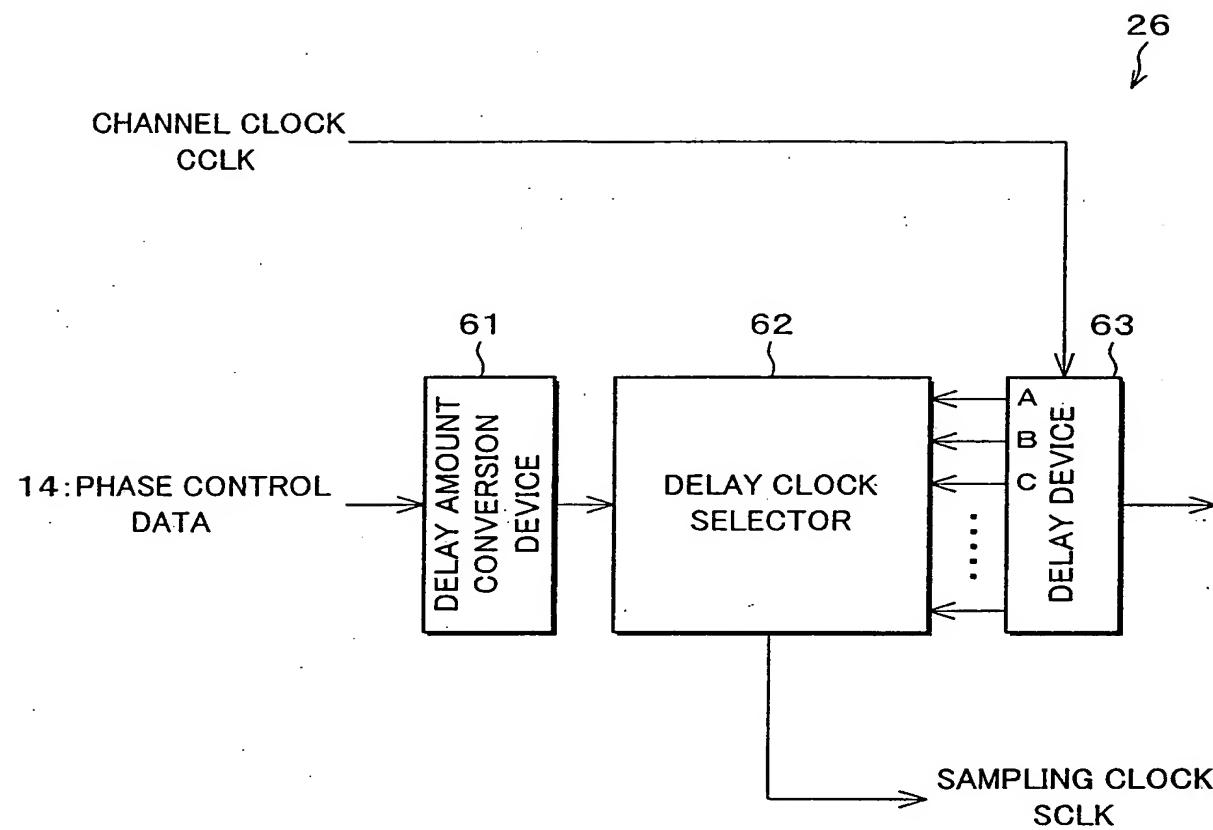
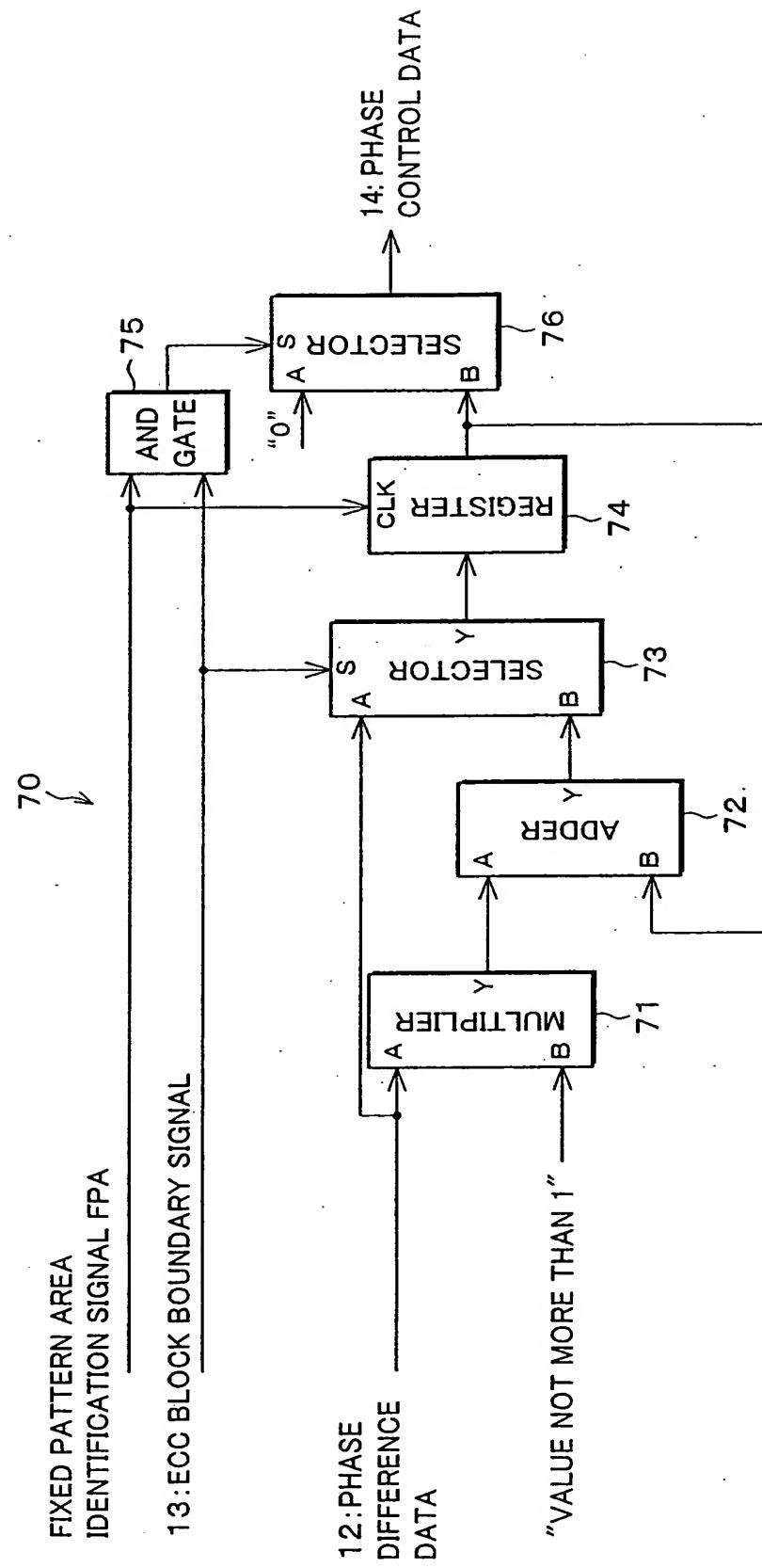




FIG. 7



**FIG.8**

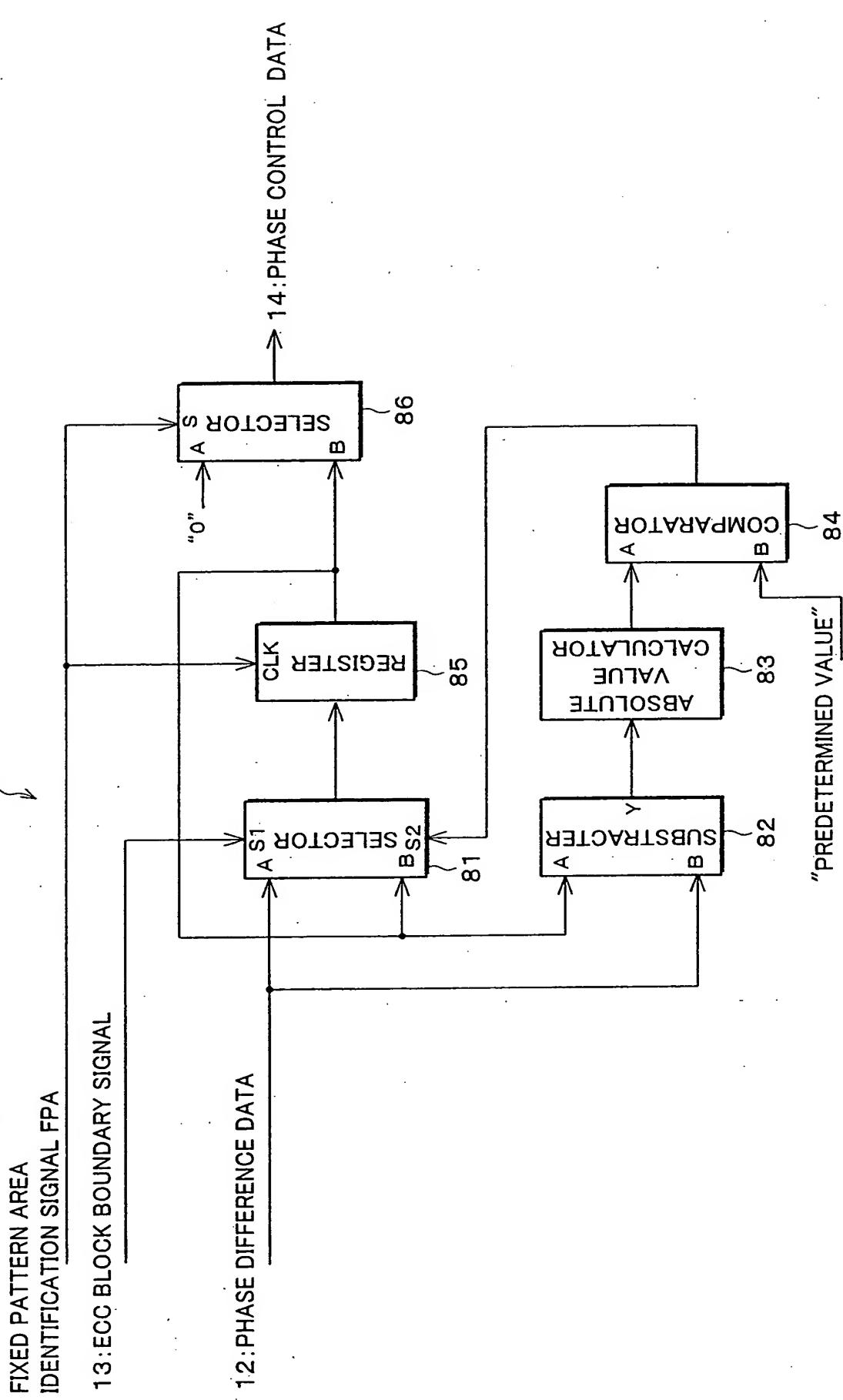
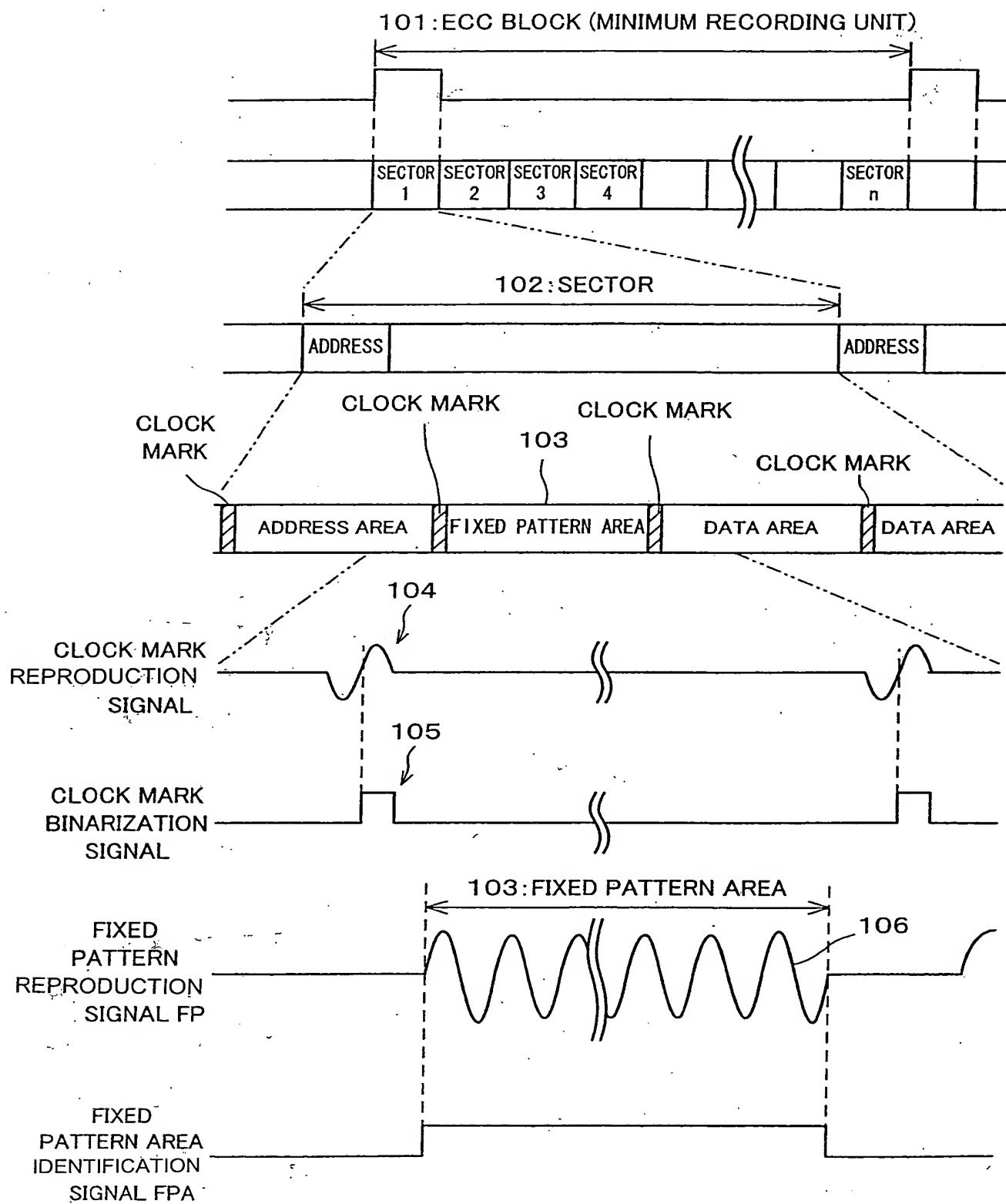




FIG. 9 PRIOR ART



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FIG. 10 PRIOR ART

